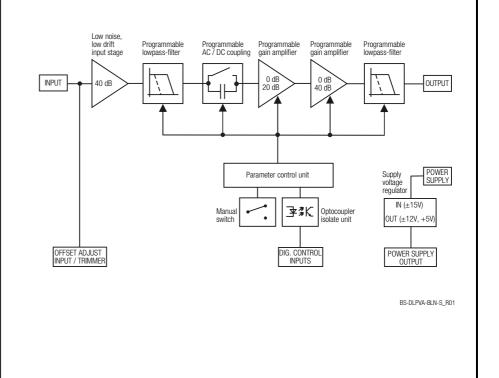
#### **DLPVA-101-BLN-S**

# Variable Gain Low-Frequency Voltage Amplifier



Features	<ul> <li>Variable gain 40 to 100 dB, switchable in 20 dB steps</li> <li>Bipolar input stage, recommended for low impedance sources less than 100 Ω</li> <li>DC-coupled, single ended</li> <li>Very low input voltage noise down to 700 pV/√Hz</li> <li>Bandwidth DC – 100 kHz, switchable to 1 kHz</li> <li>0.5 μV/°C DC-drift</li> <li>Switchable AC/DC-coupling</li> <li>Local and remote control</li> </ul>
Applications	<ul> <li>Low-noise laboratory amplifier</li> <li>Pulsed thermal EMF analysis</li> <li>Industrial sensors</li> <li>Detector preamplifier</li> <li>Integrated measurement systems</li> </ul>

Block Diagram



SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

EMTO

n	2	ta	C	h	ρ	ρ	ł
u							L

### **DLPVA-101-BLN-S**

# Variable Gain **Low-Frequency Voltage Amplifier**

Intended Use	The DLPVA-101-BLN-S voltage amplifier is a variable gain voltage amplifier. It is designed for fast amplification of small voltage signals. Operation is largely self-explanatory. If in doubt, consult this document or contact support@femto.de.  For safe operation, please refer to the damage thresholds specified in the "Absolute Maximum Ratings", "Temperature Range" and "Power Supply" sections of this document.  The operating environment must be free of smoke, dust, grease, oil, condensing moisture, and other contaminants that could affect the operation or performance.				
Application Notes	The DLPVA-101-BLN-S amplifier is designed for use with low resistance sources up to 100 $\Omega$ . A high source resistance causes significant increase of the input offset voltage and may trigger overload status. See "Overload LED" section for details.				
Available Version	DLPVA-101-BLN-S	Variable gain voltage amplifier, gain settings 40/60/80/100 dB, single ended (bipolar), typical source resistance <100 $\Omega$ , input 1 M $\Omega$ (BNC), bandwidth DC/1.5 Hz – 1/100 kHz			
Related Models	DLPVA-101-B-S	Variable gain voltage amplifier, gain settings 20/40/60/80 dB, single ended (bipolar), typical source resistance <1 k $\Omega$ , input 1 M $\Omega$ (BNC), bandwidth DC/1.5 Hz – 1/100 kHz			
	DLPVA-101-B-D	Variable gain voltage amplifier, gain settings 20/40/60/80 dB, true differential (bipolar), typical source resistance <10 k $\Omega$ , input 1 M $\Omega$ (LEMO®), bandwidth DC/1.5 Hz – 1/100 kHz			
	DLPVA-101-F-S	Variable gain voltage amplifier, gain settings 20/40/60/80 dB, single ended (FET), typical source resistance <1 M $\Omega$ , input 1 T $\Omega$ (BNC), bandwidth DC/1.5 Hz – 1/100 kHz			
	DLPVA-101-F-D	Variable gain voltage amplifier, gain settings 20/40/60/80 dB, true differential (FET), typical source resistance <1 M $\Omega$ , input 1 T $\Omega$ (LEMO®), bandwidth DC/1.5 Hz – 1/100 kHz			
	DLPVA-100-BUN-S	Ultra-low-noise variable gain voltage amplifier, gain settings 40/60/80/100 dB, single ended (bipolar), typical source resistance <50 $\Omega$ , input 1 k $\Omega$ (BNC), bandwidth 1.5 Hz – 1/100 kHz			
Available Accessories	PS-15-25-L	Power Supply Input: AC 100 – 240 V Output: DC +15 V			



Output: DC ±15 V

LUCI-10



Compact digital I/O interface for USB remote control, supports opto-isolation of amplifier signal path from PC USB port, 16 digital outputs, 3 opto-isolated digital inputs, bus-powered operation

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

Time Response

## Variable Gain Low-Frequency Voltage Amplifier

Specifications  $V_S = \pm 15 \text{ V}, T_A = 25 ^{\circ}\text{C}, \text{ output load impedance 1 M}\Omega,$ 

warm-up 20 minutes (min. 10 minutes recommended)

Gain Gain values 40, 60, 80, 100 dB

indicated by LEDs, (@ output load  $\geq$ 100 k $\Omega$ )

Gain accuracy  $\pm$  0.05 dB

Frequency Response Lower cut-off frequency DC / 1.5 Hz, switchable

Upper cut-off frequency (-3 dB) 100 kHz / 1 KHz, switchable

Upper cut-off frequency roll-off 12 dB/oct.

Rise/fall time (10 % - 90 %) 3.5 μs (@ bandwidth 100 kHz) 350 μs (@ bandwidth 1 kHz)

Input Input impedance 1 M $\Omega$  II 13 pF Input voltage drift 0.5  $\mu$ V/°C

40 dB  $0.8 \text{ nV/}\sqrt{\text{Hz}}$  60, 80, 100 dB  $0.7 \text{ nV/}\sqrt{\text{Hz}}$ 

Equ. input noise current 3 pA/ $\sqrt{\text{Hz}}$  1/f-noise corner 80 Hz Input bias current 1  $\mu$ A Input bias current drift 8 nA/°C

Input offset voltage  $\pm 0.5$  mV, adjustable by offset trimmer and

external control voltage

Output Output voltage range  $\pm 10 \text{ V } (@ \ge 100 \text{ k}\Omega \text{ output load})$ 

Output impedance 50  $\Omega$  (terminate with  $\geq$ 100 k $\Omega$  load for best performance)

 $\begin{array}{ll} \text{Max. output current} & \pm 20 \text{ mA (short-circuit proof)} \\ \text{Output overload recovery time} & 0.5 \text{ ms (after 20 x overload)} \end{array}$ 

Overload LED

The amplifier features a LED to indicate an overload condition. The Overload LED will turn on if the

signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until the Overload LED

turns off.

The Overload LED may also turn on when the amplifier is operated with open input or with a high source resistance, e. g. external AC coupling. In this case the bias current may cause a considerable input voltage. For proper operation please use a source resistance of less than

100  $\Omega$  or switch to a lower gain setting.

Digital Control Control input voltage range Low: -0.8 ...+0.8 V

High: +1.8 ... +12 V, TTL / CMOS compatible

Control input current 0 mA @ 0 V, 1.5 mA @ +5 V, 4.5 mA @ +12 V Overload output Non active: +5 V, max. 1 mA, active: 0.8 V, max. -10 mA

Ext. Offset Control Offset control voltage range ±10 V (+10 V corresponds to +0.5 mV input offset voltage)

Offset control input impedance 200 k $\Omega$ 

Power Supply Voltage DC  $\pm 15$  V ( $\pm 14.5$  V to  $\pm 16$  V)

Supply current  $\pm 75$  mA typ. (depends on operating conditions,

recommended power supply capability min. ±150 mA)

Case Weight 320 g (0.7 lbs)

Material AIMg4.5Mn, nickel-plated

Temperature Range Storage temperature  $-40 \,^{\circ}\text{C} \dots +80 \,^{\circ}\text{C}$  Operating temperature  $0 \,^{\circ}\text{C} \dots +60 \,^{\circ}\text{C}$ 

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

F E M T O

#### **DLPVA-101-BLN-S**

## Variable Gain Low-Frequency Voltage Amplifier

Absolute Maximum Ratings
Digital control input voltage
Analog control input voltage

-5 V/+16 V relative to digital ground DGND (pin 9)
±15 V relative to analog ground AGND (pin 3)

Power supply voltage  $\pm 20 \text{ V}$ Signal Input voltage  $\pm 0.7 \text{ V}$ 

Overvoltage at the signal input can severely degrade the noise performance or destroy the

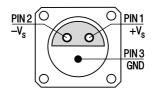
amplifier!

Connectors Input BNC jack (female)

Output BNC jack (female)

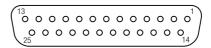
Power supply LEMO® series 1S, 3-pin fixed socket

(mating plug type: FFA.1S.303.CLAC52)



Pin 1: +15 V Pin 2: -15 V Pin 3: ground (GND)

Control port Sub-D 25-pin, female, qual. class 2



Pin 1: +12 V (stabilized power supply output\*)
Pin 2: -12 V (stabilized power supply output\*)

Pin 3: AGND (analog ground for pins 1 – 8)
Pin 4: +5 V (stabilized power supply output\*)
Pin 5: digital output: overload (referred to pin 3)

Pin 6: NC

Pin 8: input offset control voltage

Pin 9: DGND (ground for digital control pins 10 - 14)

Pin 10: NC

Pin 7:

Pin 11: digital control input: gain, LSB
Pin 12: digital control input: gain, MSB
Pin 13: digital control input: AC/DC

Pin 14: digital control input: 100kHz / 1 kHz

Pin 15 – 25: NC

\*stabilized power supply output current

±12 V: max. ±100 mA +5V: max. 50 mA

## **DLPVA-101-BLN-S**

# Variable Gain Low-Frequency Voltage Amplifier

Remote Control Operation	General	Remote control input bits are opto-isolated and connected by logical OR function to local switch settings. For remote control set the corresponding local switches to "0 dB", "AC" and "1 kHz" and select the wanted setting via a bit code at the corresponding digital inputs.  Mixed operation, e.g. local gain setting and remote controlled bandwidth setting, is also possible.				
	Gain setting	Gain Pin 11 Pin 12 LSB MSB  40 dB low low 60 dB high low 80 dB low high 100 dB high high				
	AC/DC setting  Bandwidth setting	Coupling Pin 13  AC low DC high  Bandwidth Pin 14  1 kHz low 100 kHz high				
Scope of Delivery	DLPVA-101-BLN-S, LEMO® 3-pin connector, datasheet, transport package					
Ordering Information	DLPVA-101-BLN-S	Variable gain voltage amplifier, single ended (bipolar)				
Typical Performance Characteristics	DLPVA-101-BLN-S frequency r  120 100 80 90 90 90 100 100 101	Bandwidth settings: solid line 100kHz, dashed line 1 kHz  10 <sup>2</sup> 10 <sup>3</sup> 10 <sup>4</sup> 10 <sup>5</sup> 10 <sup>6</sup> Frequency in Hz				

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

F E T O

#### **DLPVA-101-BLN-S**

# **Variable Gain Low-Frequency Voltage Amplifier**

Dimensions

all dimensions in mm unless otherwise noted

FEMTO Messtechnik GmbH Klosterstr. 64 10179 Berlin · Germany Phone: +49 30 280 4711-0 Fax: +49 30 280 4711-11 Email: info@femto.de www.femto.de

Specifications are subject to change without notice. Information provided herein is believed to be accurate and reliable. However, no responsibility is assumed by FEMTO Messtechnik GmbH for its use, nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of FEMTO Messtechnik GmbH. Product names mentioned may also be trademarks used here for identification purposes only.

© by FEMTO Messtechnik GmbH · Printed in Germany

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

0